ADVANCE NOTICE CALL FOR PAPERS

32nd Norchip Conference

27-28 October 2014, Tampere, Finland



The NORCHIP conference is the main microelectronics event of the Nordic countries. The annual IEEE CAS technically sponsored conference covers all areas of microelectronics, spanning from large digital systems to simple analog circuits. The wide scope of NORCHIP is intentional promoting cross-field collaboration. NORCHIP is a well established conference with representation from both academia and industry. Papers of the highest scientific and technical quality are presented together with selected invited speakers and pre-conference tutorial sessions.

Technical programme

The 32nd NORCHIP conference will take place on 27-28 October 2014. The Management Committee invites people from academia and industry in all Europe to contribute papers. Contributions for the 2014 conference in Tampere should be in the following areas:

- Digital and analog Microelectronics/nanoelectronics
- Embedded systems, FPGA and rapid prototyping
- Biomedical Circuits and Systems
- Sensors, Imagers, MEMS devices
- Sensor interface circuits, data-converters
- Measurement, modeling, and simulation of hardware
- Mixed mode designs, high frequency circuits, design for testability
- CAE, design tools, VLSI design methodology, neural networks
- Packaging and interconnect
- Network-on-Chip
- Wireless Circuits and Systems

Papers may be selected for either lecture or poster presentation. Equal quality measures will be applied to posters and lectures. All accepted papers will be published in the conference USB proceedings and on IEEE Xplore.

Papers will be selected on the basis of a full length paper (4 A4 pages, additional 2 pages can be acquired for 100 EUR) by the Management Committee based on a peer review made by the Review Committee. Papers should be submitted no later than 15 August

Submission of Contributions

The USB proceedings will be composed from camera ready pdf files supplied by authors. The paper must follow the IEEE standard as described in the "submission of contributions" section at www.norchip.org.

Submitted papers must not exceed four (six) A4 pages with all illustrations and references included. Papers should be submitted as a pdf file no later than 15 August 2014 (see instructions on www.norchip.org).

Authors will be notified in the beginning of October 2014. Papers selected for presentation will be presented in USB conference proceedings which will be distributed upon registration. Proceedings and all presentations will be in English.

Location

Tampere Hall Yliopistonkatu 55

FI-33101 Tampere www.tamperehall.com

In 2014 we are co-located with the International Symposium on System-on-Chip (SoC 2014) taking place on 28-29 October, 2014

www.norchip.org

CONFERENCE SECRETARIAT

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Norchip

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